



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
-----------------	-------------	----------------------	---------------------	------------------

10/575,365

04/11/2006

Munetaka Watanabe

Q78082

9055

23373 7590 05/28/2008
SUGHRUE MION, PLLC
2100 PENNSYLVANIA AVENUE, N.W.
SUITE 800
WASHINGTON, DC 20037

EXAMINER

HSIEH, HSIN YI

ART UNIT

PAPER NUMBER

2811

MAIL DATE

DELIVERY MODE

05/28/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/575,365	Applicant(s) WATANABE, MUNETAKA	
	Examiner Hsin-Yi (Steven) Hsieh	Art Unit 2811	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 29 February 2008.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1,4-9,12 and 13 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,4-9,12 and 13 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 29 February 2008 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Specification

1. The amendment filed 02/29/2008 is objected to under 35 U.S.C. 132(a) because it introduces new matter into the disclosure. 35 U.S.C. 132(a) states that no amendment shall introduce new matter into the disclosure of the invention. The added material which is not supported by the original disclosure is as follows: Fig. 3, which introduces too many features not supported by the original disclosure and should be removed.

Applicant is required to cancel the new matter in the reply to this Office Action.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

Art Unit: 2811

4. **Claims 1, 4-9, and 12-13** are rejected under 35 U.S.C. 103(a) as being unpatentable over Kamimura et al. (JP 2002368271 A).

5. Regarding **claim 1**, Kamimura et al. teach a flip-chip-type gallium nitride compound semiconductor light-emitting device (flip-chip type III nitride compound light emitting device; Abstract) comprising a substrate (11; Drawing 1, paragraph [0019]), an n-type semiconductor layer (n-type layer 13; Drawing 1, paragraph [0019]), a light-emitting layer (the layer 14 containing the layer which emits light; Drawing 1, paragraph [0020]), and a p-type semiconductor layer (p type layer 15; Drawing 1, paragraph [0021]), wherein a negative electrode (n lateral electrode film 21; Drawing 1, paragraph [0023]) provided on said n-type semiconductor layer (13; see Drawing 1), and a positive electrode (p lateral electrode film 20; Drawing 1, paragraph [0023]) provided on said p-type semiconductor layer (15; see Drawing 1), the n-type semiconductor layer (13), the light-emitting layer (14), and the p-type semiconductor layer (15) being successively provided atop said substrate (11) in this order (see Drawing 1) and being composed of a gallium nitride compound semiconductor (paragraph [0019-0023]), wherein said positive electrode (20) has a three-layer structure (18p, 20a, and 20b) comprising an ohmic electrode layer composed of rhodium (p electrode 18 which consists of Rh; Drawing 1, paragraph [0022]) which is in contact with said p-type semiconductor layer (15; see Drawing 1), an adhesion layer composed of titanium (a substrate layer 20a which consist of Ti; Drawing 1, paragraph [0023]) which is provided on said ohmic electrode layer (18p; see Drawing 1), and a bonding pad layer (the upper layer 20b; Drawing 1, paragraph [0023]) provided on said adhesion layer (20a; see Drawing 1) and being composed of a metal selected from the group consisting of

Art Unit: 2811

gold, aluminum, nickel, and copper, or composed of an alloy containing at least one of these metals (Au, i.e. gold; paragraph [0023]).

Kamimura et al. do not teach an adhesion layer has a thickness of 1000 Å to 3,000 Å as claimed, but teach an adhesive layer (20a) has a thickness of 10 Å to 1,000 Å (1 nm – 100 nm; paragraph [0010]), which overlaps the claimed range of 1000 Å to 3,000 Å, and this establishes a prima facie case of obviousness (see MPEP 2144.05).

6. Regarding **claims 4 and 5**, Kamimura et al. do not teach said ohmic electrode layer (18) has a thickness of, regarding to **claim 4**, 100 Å to 3,000 Å, and regarding to **claim 5**, 500 Å to 2,000 Å.

The parameters such as thickness of the ohmic electrode layer in the art of semiconductor manufacturing process are subject to routine experimentation and optimization to achieve the desired film quality during device fabrication. Therefore, it would have been obvious to one of the ordinary skill in the art at the time the invention was made to incorporate the thickness of the ohmic electrode layer within the range as claimed in order to form a high quality film.

7. Regarding **claim 6**, Kamimura et al. also teach A flip-chip-type gallium nitride compound semiconductor light-emitting device according to claim 1, wherein said bonding pad layer (20b) has a thickness of at least 1,000 Å (1 micrometer, i.e. 10,000 Å; paragraph [0023]).

8. Regarding **claim 7**, Kamimura et al. teach said bonding pad layer (20b) has a thickness of 0.1 micrometers – 50 micrometers, i.e. 1,000 Å – 500,000 Å (paragraph [0010]), which overlaps the claimed range of 3,000 Å to 5,000 Å. This establishes a prima facie case of obviousness (see MPEP 2144.05 I).

Art Unit: 2811

9. Regarding **claim 8**, Kamimura et al. also teach a flip-chip-type gallium nitride compound semiconductor light-emitting device according to claim 1, wherein said bonding pad layer (20b) is composed of gold (paragraph [0023]).

10. Regarding **claim 9**, Kamimura et al. also teach a positive electrode (p lateral electrode film 20; Drawing 1, paragraph [0023]) for use in a gallium nitride compound semiconductor light-emitting device (flip-chip type III nitride compound light emitting device; Abstract), wherein said positive electrode (20) has a three-layer structure (18p, 20a, and 20b) comprising an ohmic electrode layer composed of rhodium (p electrode 18 which consists of Rh; Drawing 1, paragraph [0022]) which is brought into contact with a p-type semiconductor layer (p type layer 15; Drawing 1, paragraph [0021]) of said gallium nitride compound semiconductor light-emitting device (see Drawing 1), an adhesion layer composed of titanium (a substrate layer 20a which consist of Ti; Drawing 1, paragraph [0023]) which is provided on said ohmic electrode layer (18p; see Drawing 1), and a bonding pad layer (the upper layer 20b; Drawing 1, paragraph [0023]) provided on said adhesion layer (20a; see Drawing 1), said bonding pad layer (20b) being composed of a metal selected from the group consisting of gold, aluminum, nickel, and copper, or composed of an alloy containing at least one of these metals (Au, i.e. gold; paragraph [0023]).

Kamimura et al. do not teach an adhesion layer has a thickness of 1000 Å to 3,000 Å as claimed, but teach an adhesive layer (20a) has a thickness of 10 Å to 1,000 Å (1 nm – 100 nm; paragraph [0010]), which overlaps the claimed range of 1000 Å to 3,000 Å, and this establishes a prima facie case of obviousness (see MPEP 2144.05).

Art Unit: 2811

11. Regarding **claim 12**, Kamimura et al. also teach a light-emitting diode (a light emitting device 1; Drawing 2, paragraph [0023]) comprising a flip-chip-type gallium nitride compound semiconductor light-emitting device according to claim 1.

12. Regarding **claim 13**, Kamimura et al. also teach a lamp (flip chip type LED 2; Drawing 2, paragraph [0023]) comprising a flip-chip-type gallium nitride compound semiconductor light-emitting device according to claim 1.

Response to Arguments

13. Applicant's amendments, filed 02/29/2008, overcome the objections to the drawings and claim 3, and the rejections to claims 1-13 under 35 U.S.C. 112. The objections to the drawings and claim 3, and the rejections to claims 1-13 under 35 U.S.C. 112 have been withdrawn.

14. Applicant's arguments with respect to claims 1-13 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

15. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a).

Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period

Art Unit: 2811

will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hsin-Yi (Steven) Hsieh whose telephone number is 571-270-3043. The examiner can normally be reached on Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne A. Gurley can be reached on 571-272-1670. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Lynne A. Gurley/
Supervisory Patent Examiner, Art Unit 2811

/H. H./
Examiner, Art Unit 2811
5/13/2008